***Important specifications:***

* System clock = 8MHz
* ADC resolution = 10-bit
* ADC clock = 250kHz
* ADC sampling every 1us using Timer/Counter1*(16-bit)* CTC comp\_B (needs check)
* ADC sampling size = 10 readings per channel
* ADC reference voltage = AVCC = 3.3V
* USART0 – 8N1 – no parity
* Baud rate = 9600 (standard)
* Transmitting data to terminal every 1s using Timer/Counter3*(16-bit)* OVF
* Timer/Counter0, 2 are used for PWM signal generation to control the drivers for the H-bridge

***Work in progress:***

* Implementing ramping down of the PWM signals for over voltage, current and temperature
* Determining communication protocols with the Bluetooth module